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CERTIFICATE

This is to certify that the thesis entitled, “A nine level cascaded multi level inverter with reduced switch count and lower harmonics” submitted by **Abhista Gorityala & Kavadam Lakshmi Kantham** in partial fulfillment of the requirement for the award of Bachelor of Technology degree in Electronics and Communication Engineering at Department of Electronics and Communication Engineering, School of Studies in Engineering and Technology Guru Ghasidas Vishwavidyalaya Central University Bilaspur Chhattisgarh is an authentic work carried out by them during a period from February to July 2021 (VIII semester) under the supervision and guidance of **Mrs. Anita Khanna** and **Dr. Anil Kumar Soni** Electronics and Communication Engineering. To the best of my knowledge, the matter embodied in the thesis has not been submitted to any other University/ Institute for the award of any Degree or Diploma.

(Examination of Project is conducted by online mode due to COVID 19 pandemic.)

Date 23 July 2021

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