

CERTIFICATE NO : PS-PCBESDFPGA-ESD-0002

PANTECHSOLUTIONS®
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CERTIFICATE OF INTERNSHIP

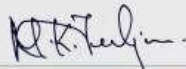
NAME : Ms.KAKARLA YUGALA

COLLEGE : GURU GHASIDAS VISHWAVIDYALAYA

has Successfully Completed
INTERNSHIP ON EMBEDDED SYSTEM DESIGN AND IOT (30 DAYS)

at Pantech Prolabs India Pvt Ltd

From : 01-AUG,2021 to : 30-AUG,2021



M.K.JEEVARAJAN

DIRECTOR

PANTECHSOLUTIONS

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