



List of New Course(s) Introduced

Department : **Electronics and Communication Engineering**

Programme Name : **B.Tech.**

Academic Year : **2016-17**

List of New Course(s) Introduced

Sr. No.	Course Code	Name of the Course
01.	EC3THS03	Engineering Economics
02.	EC3TPC01	Signals and Systems
03.	EC3TBS01	Engineering Mathematics-III
04.	EC3TES01	Network Analysis And Synthesis
05.	EC3TES02	Electronic Devices
06.	EC3TPC02	Digital Logic Circuits
07.	EC4TBS02	Numerical Analysis
08.	EC4TPC03	Automatic Control Systems
09.	EC4TPC04	Analog Circuits
10.	EC4TPC05	Communication System-I
11	EC4TPC06	Electronics Measurements & Instrumentation

वर्षगाध्यक्ष (इले. एव संचार अभियंत्रिकी)
H.O.D. (Elect. & Comm. Engineering)
प्रौद्योगिकी संस्थान
Institute of Technology
गु. घा. वि., बिलासपुर (छ.ग.)
G. G. V. Bilaspur (C.G.)



Minutes of Meetings (MoM) of Board of Studies (BoS)

Academic Year : 2016-17

School : School of Studies of Engineering and Technology

Department : Electronics and Communication Engineering

Date and Time : May 12, 2016 - 11:00 AM

Venue : E-Class Room

The scheduled meeting of member of Board of Studies (BoS) of Department of Electronics and Communication Engineering, School of Studies of Engineering and Technology, Guru Ghasidas Vishwavidyalaya, Bilaspur was held to design and discuss the B. Tech. Second year (III and IV semesters) scheme and syllabi.

The following members were present in the meeting:

1. Prof. Shrish Verma (External Expert Member BoS, Dept. of ECE, NIT Raipur)
2. Dr. Dr. P.S. Shrivastav (HOD, Assistant Prof., Dept. of ECE-cum Chairman, BOS)
3. Mr. Abhishek Awasthi (Member BoS, Assistant Professor, Dept. of ECE)
4. Mrs. B. Nath (Invited Member, Assistant Professor, Dept. of ECE)
5. Mrs. P. Patharia (Invited Member, Assistant Professor, Dept. of ECE)
6. Mr. Deepak K. Rathore (Invited Member, Assistant Professor, Dept. of ECE)
7. Mr. Shrawan K. Patel (Invited Member, Assistant Professor, Dept. of ECE)
8. Dr. Soma Das (Invited Member, Assistant Professor, Dept. of ECE)
9. Mrs. P. Rajput (Invited Member, Assistant Professor, Dept. of ECE)
10. Mr. Upendra Agrawal (Invited Member, Assistant Professor, Dept. of ECE)

Following points were discussed during the meeting

1. CBCS based evaluation scheme of 3rd and 4th semester was discussed and finalized
2. Courses of 3rd and 4th semester are discussed one by one and the changes have been incorporated as per the valuable suggestions of Expert member.

The following new courses were introduced in the of B. Tech. Second year (III and IV semesters):

- ❖ Engineering Economics (EC3THS03)
- ❖ Signals and Systems (EC3TPC01)
- ❖ Engineering Mathematics-III (EC3TBS01)
- ❖ Network Analysis And Synthesis (EC3TES01)
- ❖ Electronic Devices (EC3TES02)
- ❖ Digital Logic Circuits (EC3TPC02)
- ❖ Numerical Analysis (EC4TBS02)
- ❖ Automatic Control Systems (EC4TPC03)
- ❖ Analog Circuits (EC4TPC04)
- ❖ Communication System-I (EC4TPC05)
- ❖ Electronic Measurements and Instrumentation (EC4TPC06)

वर्तमानाध्यक्ष (इले. एवं संचार अभियंत्रिकी)
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G. G. V. Bilaspur (C.G.)
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Scheme and Syllabus

INSTITUTE OF TECHNOLOGY
GURU GHASIDAS CENTRAL UNIVERSITY, BILASPUR
SCHEME OF B.Tech.IIIrd SEMESTER (CBCS)
ELECTRONICS & COMMUNICATION ENGINEERING

IIIrd SEMESTER

S. No	Subject Code	Subject	Periods			Evaluation Scheme			Credit
			L	T	P	IA	ESE	Sub Total	
1	EC3THS03	Engineering Economics	3	0	0	40	60	100	3
2	EC3TPC01	Signals & Systems	3	1	0	40	60	100	4
3	EC3TBS01	Engineering Mathematics - III	3	1	0	40	60	100	4
4	EC3TES01	Network Analysis And Synthesis	3	1	0	40	60	100	4
5	EC3TES02	Electronic Devices	3	1	0	40	60	100	4
6	EC3TPC02	Digital Logic Circuits	3	1	0	40	60	100	4
7	EC3PES02	Electronic Devices Lab	-	-	3	30	20	50	2
8	EC3PPC02	Digital Logic Circuits Lab	-	-	3	30	20	50	2
			18	5	6	300	400	700	27

L: Lecture, T: Tutorial, P: Practical, IA: Internal Assessment, MSE: Mid Semester Exam, ESE: End Semester Exam

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INSTITUTE OF TECHNOLOGY
GURU GHASIDAS CENTRAL UNIVERSITY, BILASPUR
SCHEME OF B.Tech. IVth SEMESTER (CBCS)
ELECTRONICS & COMMUNICATION ENGINEERING

S. No	Subject Code	Subject	Periods			Evaluation Scheme			Credit
			L	T	P	IA	ESE	Sub Total	
1.	EC4TBS02	Numerical Analysis	3	1	0	40	60	100	4
2.	EC4TPC03	Automatic Control Systems	3	1	0	40	60	100	4
3.	EC4TPC04	Analog Circuits	3	1	0	40	60	100	4
4.	EC4TPC05	Communication System-I	3	1	0	40	60	100	4
5.	EC4TPC06	Electronic Measurements & Instrumentation	3	0	0	40	60	100	3
6.	EC4PPC04	Analog Circuits Lab	0	0	3	30	20	50	2
7.	EC4PPC05	Communication System-I Lab	0	0	3	30	20	50	2
8.	EC4PPC06	Electronic Measurements & Instrumentation Lab	0	0	3	30	20	50	2
			15	5	9	290	360	650	25

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC3THS03	3	0		3 hours	40	60	3

ENGINEERING ECONOMICS

Unit 1: Basic Concepts and Definitions, Methodology of Economics, Demand and Supply - elasticity, Theory of the Firm and Market Structure, Price and output determinations in different types of market

Unit 2: Public Sector Economics - Welfare economics, Central and commercial marks and their functions. Industrial policies, theory of localization, weber & surgent Florence theory, investment analysis-NPV, ROI, IRR, Payback period, SWOT analysis.

Unit 3: Monetary and Fiscal Policy: Tools, impact on the economy, Inflation, Business Cycle, Cash Flow-2,3,4 Model.

Unit 4: Business Forecasting - Elementary techniques. Cost and Revenue Analysis, Capital Budget, Break Even Analysis.

Unit 5: Indian economy; Urbanization, Unemployment-Poverty, Regional Disparities, Unorganized Sectors- Roll of Plans, Reforms-Post Independent period.

SUGGESTED TEXT BOOKS:-

1. Mankiv Gregory N.(2002), Principles of Economics, Thompson Asia
2. V. Mote, S. Paul, G. Gupta(2004), Managerial Economics, Tata McGraw Hill

REFERENCE BOOKS:-

1. Misra, S.K. and Puri (2009), Indian Economy, Himalaya
2. PnreckSaroj (2003), Textbook of Business Economics, Sunrise Publishers

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC3TPC01	3	1		3 hours	40	60	4

SIGNALS & SYSTEM

UNIT - I

Signals & Systems: Classification of Signals, Classification of systems, Properties of systems - Invertibility, Causality, Stability, Time Invariance, Linearity; Time domain Analysis of Discrete time and Continuous time System - Natural and Forced Response, Impulse Response and Convolution, Properties of Convolution, Step Response, Systems described by difference and differential equations, Eigen values and Eigen functions.

UNIT - II

Analysis of Continuous time Signals: Fourier analysis of Continuous time LTI system, Fourier series Representation of Periodic signals: CTFS, Representation of Aperiodic Signals: CTFT, CTFT of Periodic Signals, Properties of CTFT, System Analysis with Fourier Transform, Analysis of Discrete time Signals: Frequency Response of Discrete time LTI system, Representation of Periodic signals: DTFS, Representation of Aperiodic Signals: DTFT, DTFT of Periodic Signals, Properties of DTFT, Frequency response of discrete time LTI systems.

UNIT - III

Review of Laplace transforms, Z-Transform: Z-transforms of common sequences, Properties of Z-transforms, Region of Convergence, Inverse Z-transforms, Analysis of discrete time systems using Z-transforms, Relation between Z and Laplace Transform, Relation between Z-Transform and DTFT.

UNIT - IV

DFT & Fast Fourier Transform: Introduction to DFT, Properties of DFT, Circular Convolution, Introduction to FFT, Decimation in Time Algorithm, Decimation in Frequency Algorithm, Difference & similarities between DIT & DIF Algorithm, IDFT using FFT Algorithm.

UNIT - V

State space Analysis: Block diagram presentation of LTI Systems, System Realization of Continuous and Discrete time systems, State Space analysis of continuous time LTI systems, solutions of state equation for continuous time LTI systems, State Space analysis of discrete time LTI systems, solutions of state equation for discrete time LTI systems.

SUGGESTED TEXT BOOKS:-

1. Signals and Systems, by Simon Haykin and Barry Van Veen, Wiley, 1999.
2. Signal & System, Samarjit Ghosh, TMH.
3. Signal & System, P Ramesh Babu, Scitech Publication

REFERENCE BOOKS:-

1. Signal & System, A V Oppenheim, PHI
2. Signal & System, F Hussain, Umesh Publication
3. Schaum's Outline of Signals and Systems - H Hsu, TMH.

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC3TBS01	3	1		3 hours	40	60	4

ENGINEERING MATHEMATICS - III

UNIT - I

Functions of Complex Variables: Limit, Derivative, Analytic function, Cauchy-Riemann Equations, Harmonic Functions, Geometrical representation, Transformation, Bilinear Transformation, Application to Flow problems, Complex Integration, Cauchy's integral theorem, and Integral formula, Taylor's & Laurent's series, Singular point, Poles & residues, Residue theorem & its application to contour integration.

UNIT - II

Fourier Series: Periodic Functions, Definition of Fourier series, Euler's formulae, Dirichlet conditions, Change of interval, Even and Odd Functions, Half Range Fourier Sine & Cosine series, Parseval's identity, Practical Harmonic Analysis.

UNIT - III

Laplace Transform: Definition, Linearity, Shifting & Scaling properties, Transform of Elementary functions, Transform of Derivatives & Integrals, Multiplication by t & division by t , Inverse Laplace transform, Convolution theorem, Transform of Periodic functions, Unit Step function & Dirac delta function, Initial value and Final value theorems, Application to solution of ordinary differential equations.

UNIT - IV

Fourier Transform: Definition of Fourier Integrals- Fourier Sine & Cosine integrals, Complex form of Fourier integral, Fourier Sine & Cosine transforms, Complex form of Fourier Transform, Linearity, Shifting & Scaling properties, Modulation theorem, Inverse Fourier transform, Fourier transform of derivatives.

UNIT - V

Theory of Probability: Mathematical and Statistical definition of Probability, Addition Law of Probability, Multiplication Law of Probability, Conditional Probability, Bayes Theorem, Binomial Distribution, Poisson Distribution, Normal Distribution.

SUGGESTED TEXT BOOKS:-

1. H K Das, "Advance Engg. Mathematics", S-Chand Publication
2. B S Grewal, "Higher Engg. Mathematics", Khanna Publication
3. Erwin Kreyszig, "Advance Engg. Mathematics", J Wiley & Sons

REFERENCE BOOKS:-

1. Louis A Pipes, "Applied Mathematics for Engineers & Physicists", TMH
2. R M Rao & A S Bopardikar, "Wavelet Transforms- Introduction to Theory and Applications Advance Engg. Mathematics"
3. Burrus Sidney, R A Gopinath, Guo Haitao, "Introduction to Wavelets and Wavelet Transforms", Printice Hall International.
4. Y T Chan, "Wavelet Basics", Kluwer Academic Publishers
5. Lokenath Debnath, "Wavelet Transforms and their applications"



Sub Code	L	T	P	Duration	IA	ESE	Credits
EC3TES01	3	1		3 hours	40	60	4

NETWORK ANALYSIS AND SYNTHESIS

UNIT - I

Review of Circuit concept, Network Graph Theory: Introduction, Concept of Network Graph, Terminology Used in Network Graph, Properties of Tree in a Graph, Formation of Incidence Matrix, Properties of Incidence Matrix, Number of Tree in a Graph, Cut Set Matrix, Loop Matrix, Interrelation among various Matrices, Sinusoidal steady state analysis of R, C L circuits, Excitation and Resonance

UNIT - II

First order differential equations: General & Particular solutions, time constants, Integration factor, Initial factor, Initial conditions in Networks: Why Study Initial Conditions. Initial Conditions in Element, Geometrical Interpretation Of Derivatives, A Procedure for Evaluating Initial Conditions, initial State of a Network, Second order differential equations: Internal Excitation, Network excited by external energy sources, Responses as related to the s-plane location of roots, General solutions in terms of S, Q and Wn, Review of Laplace transformations

UNIT - III

Impedance Function: Complex frequency, Transform Impedance and transform circuits: Impulse, Unit Step, Ramp and Gate function; Waveform synthesis, Network Theorem: Thevenin's & Norton's theorem, Superposition, Reciprocity, Maximum Power transfer and Millman's theorem, Tellegen's theorem.

UNIT - IV

Poles and Zeros of Network function: Restrictions on poles & zeros, Locations for transfer function & driving point functions, Time domain behavior from pole and zero plot, Stability of active network, Two port parameters: Relation of two port variables, Short circuit admittance parameter, Open circuit Impedance parameter, Transmission parameters, h-parameter, T-parameter, Relation between parameter sets, parallel connection of two Port network.

UNIT - V

Network Synthesis: Concept, Hurwitz property positive realness properties of positive real functions, Synthesis of RL, RC and LC, Driving point Impedance functions using simple canonical Networks-Foster and Cauer form.

SUGGESTED TEXT BOOKS:

1. M E Valkenburg, "Network Analysis", PHI/Pearson Edu
2. Engineering circuit analysis-Hayt and Kimberley, TMH
3. A Chakrabarty, "Circuit Theory Analysis & Synthesis", Dhunpat Rai & Co.

REFERENCES BOOKS

1. Network Theory- D. Roy Chaudhary, Newage Asian
2. Electric Circuit Analysis-Alexander and Sadique, TMH
3. Engineering circuit analysis-Hayt and Kimberley, TMH
4. A Sudhakar & Shyam Mohan S Palli, "Circuits and Networks: Analysis & Synthesis", TMH

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC3TES02	3	1		3 hours	40	60	4

ELECTRONIC DEVICES

UNIT - I

Review of Semiconductor concept, Transport Phenomena of semiconductor, Charge density in Semiconductor, Hall Effect, Injected minority charge carriers, Potential variation within graded semiconductor, Junction Diode Characteristics, Current component of PN diode, Diffusion capacitance, Junction diode switching times, Piecewise linear model, Breakdown mechanism.

UNIT - II

Diode Circuits: Load line concepts, Graphical analysis, Clipper circuit, Clamper, Comparator, Rectifier, Full wave circuits, Filter circuits: Inductor filter, Capacitor filter, LC filter, Multiple LC filter, CLC or π filter. Zener diode regulator circuit, Negative conductance in semiconductors- Tunnel diode, Photo diode - Photo voltaic effect, Solar cells, Schottky Diode, Varactor Diode, Avalanche diode, PIN diode, LED, LASER.

UNIT - III

Transistor Characteristics: Junction Transistor, Transistor current components, Transistor as an amplifier, Transistor construction, Transistor circuit configuration (CB, CE, CC)- Analytical Expression for transistor characteristics and Operation, Early Effect, Ebers-Moll Model, β -re model, Transistor as a switch, Transistor Biasing and Thermal Stabilization, Stability factor- Stabilization against variation in I_{co} , V_{BE} and β , Emitter bias, Collector - to - base bias, Voltage divider bias with emitter bias, Emitter bypass capacitor. Bias compensation.

UNIT - IV

Field Effect Transistor (FET): JFET Construction, Operation, V-I characteristics, Transfer characteristics, Drain characteristics. Metal Oxide Semiconductor Field Effect Transistor (MOSFET)- Construction, Operation and characteristics, Depletion MOSFET, Enhancement MOSFET, complementary MOSFET, Application of CMOS.

UNIT - V

Special semiconductor Devices: Bi-CMOS device, MIS diode heterojunction devices, Silicon controlled rectifier: V-I characteristics, gate triggering characteristics, Application, Silicon-controlled switch. DIAC, TRIAC, Unijunction transistors - Construction, Operation, V-I characteristics, Triggering circuit, Control, Application.

SUGGESTED TEXT BOOKS:-

1. *Integrated Electronics: Analog & Digital Circuit Systems- Jacob Millman & Halkias, TMH*
2. *Electronic Devices & Circuits- Allen Mottershead, PHI*
3. *Electronic Devices & Circuit Theory- Boylestad & Nashelsky, PHI*

REFERENCE BOOKS:-

1. *Microelectronics - Millman and Grabel, TMH*
2. *Microelectronics circuits-SedraSmith, Oxford University Press*
3. *Electronic Devices & Circuit Analysis- K Lal Kishore, BS Publications*

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC3TPC02	3	1	0	3 hours	40	60	4

DIGITAL LOGIC CIRCUITS

UNIT - I

CODES: Binary codes: Introduction & usefulness, Weighted & non-weighted codes, Sequential codes, Self complementing codes, Cyclic codes, 8-4-2-1 BCD code, Excess-3 code, Grey code; Binary to Grey and Grey to Binary code conversion, Error detecting code, Error correcting code, 7-bit Hamming code, ASCII code, EBCDIC code, Realization of Boolean Expressions: Reduction of Boolean Expressions using Laws, Theorems and Axioms of Boolean Algebra, Boolean expressions and logic diagram, Converting AND/OR/Invert logic to NAND/NOR logic, SOP and POS Forms and their Realization.

UNIT - II

Minimization Techniques: Expansion of a Boolean expression to SOP form, Expansion of a Boolean expression to POS form, 2, 3 & 4 variable K-map: Mapping and minimization of SOP and POS expressions, Completely and Incompletely Specified function-Concept of Don't Care Terms.

UNIT - III

Combinational Circuits: Adder & Subtractor: Half adder, Full adder, Half subtractor, Full subtractor, Parallel binary adder, Look Ahead carry adder, Serial adder, BCD adder, Code converter, Parity bit generator/checker, Comparator, Decoder: 3-line to 8-line decoder, 8-4-2-1 BCD to Decimal decoder, BCD to 7 segment decoder, Encoder: Octal to Binary and Decimal to BCD encoder, Multiplexer: 2-input multiplexer, 4-input multiplexer, 16-input multiplexer, Demultiplexer: 1-line to 4-line & 1-line to 8-line demultiplexer, Multiplexer as Universal Logic Function Generator, Programmed Array Logic (PAL), PLA and PLD.

UNIT - IV

Sequential Circuits: Flip-Flop & Timing Circuits: S-R Latch, Gated S-R Latch, D Latch, J-K Flip-Flop, T Flip-Flop, Edge-triggered S-R, D, J-K, T Flip-Flops, Master-Slave Flip-Flop, Direct Preset and Clear Inputs, Shift Registers: PIPO, SIPO, PISO, SISO, Bi-directional Shift Registers, Universal Shift Registers, Counter: Asynchronous Counter: Ripple Counter, Design of Asynchronous Counter, Effect of propagation delay in Ripple Counter, Synchronous Counter: 4-bit Synchronous Up Counter, 4-bit Synchronous Down Counter, Design of Synchronous Counter, Ring Counter, Johnson Counter, Pulse Train generators using Counter, Design of Sequence generator: Digital clock using counters.

UNIT - V

Digital Logic Families: Introduction, Simple Diode Gating and Transistor Inverter, Basic concepts of RTL and DTL, TTL, Open collector gates, TTL subfamilies, IIL, ECL, MOS Logic, CMOS Logic, Dynamic MOS Logic, Interfacing: TTL to CMOS, CMOS to TTL, Comparison among various logic families, Manufacturer's specification.

SUGGESTED TEXT BOOKS:

1. A Anand Kumar, "Fundamentals of Digital Circuits", PHI
2. H Taub and D Schilling, "Digital Integrated Electronics", TMH
3. Digital Logic and Computer Design, Morris Mano, PHI

REFERENCE BOOKS:

1. An Engineering Approach to Digital Design, W. Fletcher, PHI Edition
2. Floyd & Jahn, "Digital Fundamentals", Pearson Edu

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC4TBS02	3	1		3 hours	40	60	4

NUMERICAL ANALYSIS

UNIT - I

Approximations and Errors in Computations: Errors and their analysis, Types of Errors, General Error-formula, Errors in numerical computation, Curve Fitting: Methods of Least squares, Fitting of a straight line, Fitting of an exponential curves, Polynomial Fit, Non Linear Regression (2nd degree parabola), Least squares Approximation, Method of moments.

UNIT - II

Numerical Solution of Algebraic and Transcendental Equations: Graphical Method, Bisection Method, Secant Method, Regula-falsi Method, Newton Raphson Method, Iteration Method AITKEN'S Method, Newton rate of convergence. Solution of a system of simultaneous linear algebraic Equations Direct method; Gauss elimination method, Gauss Jordan Method, Triangularization Method, Crout's Method, Cholesky Method, Ill conditioned system of equation and refinement of solution, Iterative Methods: Jacobi Iterative Method, Gauss Siedel Iterative Method, Successive Over Relaxation (SOR) Method.

UNIT - III

The Calculus of Finite Differences: Finite differences, Difference formula, Operators and relation between operators, Differences of a polynomial factorial polynomial, Effect of an error on a difference table, Inverse operator, Interpolation with equal intervals: Newton's forward and backward interpolation formula, Central difference interpolation formula: Gauss's forward and backward interpolation formula, Sterling's formula, Bessel's formula, Laplace-Everett's formula, Choice of interpolation formula, Interpolation with Unequal intervals: Lagrange's interpolation, Newton's difference formula, Hermit's interpolation, Inverse interpolation.

UNIT - IV

Numerical Differentiation and Intergration: Numerical Differentiation, Newton's forward and backward difference interpolation formula, Maxima and Minima of a Tabulated function, Numerical Integration: Newton-Cote's quadrature formula, Trapezoidal rule, Simpson's $1/3^{rd}$ rule, Boole's rule, Weddle rule, Difference Equations: Definition, Order and degree of a difference equation, Linear difference equation, Difference equations reducible to Linear form, Simultaneous difference equations with constant coefficients, Applications.

UNIT - V

Numerical solution of ordinary differential equation: Taylor series method, Picard's method, Euler's method, Modified Euler method, Runge's method, Runge Kutta method, Predict corrector method, Milne's method, Adam-Bashforth method, Numerical solution for partial differential equations: Classification of P.D.E. of the 2nd order Elliptic equations, Solution of Laplace equation, Solution of Poisson's equation, Solution of elliptic equations by Relaxation method, Parabolic equations, Solution of one two dimensional heat equation, Hyperbolic equation, Solution of wave equations.

SUGGESTED TEXT BOOKS:-

1. Jain & Lyngar, Numerical Methods for Scientific and Engineering Computations.
2. G S Rao, Numerical Analysis.

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC4TPC03	3	1		3 hours	40	60	4

AUTOMATIC CONTROL SYSTEM

UNIT-I

Control System Component & Transfer Function: Control System Component & Transfer Function: System component, open loop and closed loop system, Introduction to feedback concept, Mathematical modeling of electrical & mechanical system, Transfer function of Linear system, Block diagram and its reduction procedure, Signal flow graph, Mason gain formula, System Components.

UNIT-II

Time Response Analysis: Time response of first and second order system, Types of systems, Steady State Error and Error Constants, Basic control action and automatic controllers, effects of proportional, integral, derivative and PID controller on system performance.

UNIT-III

Stability: Concept of stability, Necessary Condition for Stability, absolute and relative stability, Hurwitz Stability Criterion, Routh Stability Criterion, Relative stability Analysis, Root Locus Technique: Concept, Root locus techniques, Construction of Root Loci, Breakaway points, Determination of Roots from Root Locus, Root contours, Sensitivity of the Roots of the Characteristic Equation.

UNIT-IV

Frequency Domain analysis & Compensation Techniques: Correlation between time and frequency response, Polar Plots, Inverse Polar Plots, Bode Plots- details, Pole and Zero on real axis, Complex conjugate pole, Construction of Bode Plots, Compensation Network - phase lead, phase lag, lag-lead compensation, Feedback Compensation, Stability in Frequency Domain: Nyquist stability criteria, Nyquist contour, Mapping, Nyquist criteria, Assessment of relative stability using Nyquist criteria, Gain margin and Phase margin.

UNIT-V

State Variable Analysis and Design: Concept of State, State Variables and State Model for linear continuous time systems, State space representation using Phase variables, Phase variable formulations, State space representation using Canonical variables, State Variables and discrete time system, Diagonalization, Solution of State Equations, Controllability and Observability.

SUGGESTED TEXT BOOKS:-

1. "Modern Control Engineering", Ogata, Pearson Education.
2. "Control System Engineering", M Gopal, New Age International.
3. "Automatic Control System" D.C. Kuo, PHI
4. "Linear Control System", B.S. Manke, Khanna Pub.

REFERENCE BOOKS:-

1. "Modern Control System", R.C. Dorf & R.N. Bishop, AWL Low price edition.
2. "Introduction to Control Engineering", Ajit K. Mandal, New Age International.

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC4TPC04	3	1		3 hours	40	60	4

ANALOG CIRCUITS

UNIT - I

Low Frequency Transistor Amplifier: Graphical Analysis of CE amplifier; h-parameter Models for CB, CE, CC configurations and their Interrelationship; Analysis and Comparison of the three Configurations; Linear analysis of Transistor Circuits: Miller's Theorem: Cascading: Simplified Models and Calculation of CE and CC Amplifiers; Effect of emitter Resistance in CE amplifiers: Cascade amplifiers: Darlington Pair, analysis of Single stage FET amplifier-CS and CD Configuration.

UNIT - II

High Frequency Transistor Amplifier: CE hybrid pi model, Validity and parameter Variation, Current gain with Resistive load: frequency response of a single stage CE amplifier: Gain-Bandwidth product: CC stage High frequencies, Multistage Amplifier: Classification: Distortion in Amplifiers: Frequency Response: Bode Plots, Step response, Pass band of Cascaded Stages: Response of a two-stage RC coupled Amplifier at Low and High frequencies: sources of noise in transistor circuits, Noise figure.

UNIT - III

Feedback Amplifiers: Classification: Feedback concept, Ideal feedback amplifier, Properties of negative feedback amplifier topologies: Method of Analysis of feedback amplifier. Voltage series feedback: Voltage series feedback pair: Current series, current shunt, Voltage shunt feedback, Effect of feedback on amplifier bandwidth and stability.

UNIT - IV

Large Signal/ Power Amplifier: Classification, large signal amplifier characteristics, class A amplifiers: class A amplifier with direct-coupled resistive load, transformer-coupled class A amplifier, class A pushpull amplifiers, class B amplifiers- transformer-coupled push-pull class B amplifier, complementary symmetry push-pull class B amplifier, class AB amplifier, class C amplifier, Harmonic Distortion, Push-pull Amplifiers, Cross-over Distortion.

UNIT - V

Oscillator: Sinusoidal oscillator, Phase shift oscillator, Wien bridge oscillator, Resonant circuit oscillators: LC Collpit, LC Hartley, Amplitude, Frequency and Phase stability; Analysis of all oscillators. General form of oscillator configuration: Crystal oscillator, Tuned Amplifiers: Classification of tuned Amplifier, Analysis of single and double tuned amplifiers, Stagger tuned amplifier

SUGGESTED TEXT BOOKS:-

1. *Integrated Electronics, Millman & Halkias, TMH*
2. *Microelectronics, Millman & Grabel, TMH*

REFERENCE BOOKS:-

1. *Electronic Device & Circuits, David A Bell, PHI*
2. *Electronic Device & Circuit Theory, Boylestad & Nashelsky, PHI*

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Sub Code	L	T	P	Duration	IA	ESE	Credits
EC4TPC05	3	1		3 hours	40	60	4

COMMUNICATION SYSTEM - I

UNIT - I

Random Variables & Processes: Probability, Random Variables, Cumulative Distribution Function, Probability density function, average value & Variance of Random Variable, co relation between random variables, Random process, auto correlation and Power spectral density of random process, classification of random process.

UNIT - II

Amplitude Modulation: Review of Signal Analysis, Introduction to communication system, Frequency Translation, A Method of Frequency Translation, Recovery of Baseband Signal, Amplitude Modulation, Maximum Allowable Modulation, The Square-Law Demodulation, Spectrum Of An AM Signal, Modulators & Balanced Modulators, Single Sideband Modulation, Method Of Generating A DSB signal, An SSB Signal, VSB, Multiplexing, Block Diagram of AM Transmitter & super heterodyne receiver.

UNIT - III

Exponential Modulation: Phase & Frequency Modulation: Mathematical representation of FM & PM signals, Relationship Between Phase & Frequency Modulation, Phase & Frequency Deviation, Spectrum Of An FM Signal, Transmission BW of FM waves, Phasor Diagram For FM waves, WBFM & NBFM, Generation of FM waves: Indirect FM (Armstrong Method), Direct FM, Demodulation of FM waves, Balanced frequency discriminator - Zero-crossing detector, comparison of AM and FM systems. Block Diagram of FM Transmitter & Receiver.

UNIT - IV

Mathematical Representation of Noise: Sources of noise, Frequency domain Representation of Noise, spectral component of noise, effect of filter on PSD of noise, superposition of noise, quadrature component of noise, resistor noise, available power, noise temperature, noise figure, two port cascaded systems, noise bandwidth, effective input noise temperature, White noise.

UNIT - V

Noise in CW Modulation: AM Receiver model, Signal to noise ratios for coherent reception, DSB-SC receiver, SSC-SC receiver, Noise in AM receivers using envelope detection, AM threshold effect, FM receiver model, Noise in FM reception, Capture effect in FM, Threshold effect, FM threshold reduction, Pre-emphasis and De-emphasis in FM.

SUGGESTED TEXT BOOKS:-

1. "Principles of Communication System", Tmb & Schilling, TMH
2. "Electronic Communication System", George Kennedy, TM
3. "Principles of Communication Systems", Simon Haykin, John Wiley, 2nd Ed.

REFERENCE BOOKS:-

1. "Communication System", R P Singh & S D Sapre, TMH
2. "Modern Analog and Digital Communication", B.P Lathi 3rd edition, Oxford Press

Sharma
Adarsh



Sub Code	L	T	P	Duration	IA	ESE	Credits
EC4TPC06	3	0	0	3 hours	40	60	3

ELECTRONIC MEASUREMENTS & INSTRUMENTATION

UNIT - I

Measurements, Significance of measurement, Methods of measurement, Instruments and measurement system, Classification of Instruments, Mode of Operation, Application of measurement system, Characteristics of instrument and measurement system; Elements of a Generalized Measurement System, Accuracy and precision, Significant figure, types of error, Probability of error.

UNIT - II

Electromechanical Indicating Instruments: Operating forces, Constructional Details, Types of Support, Torque/Weight Ratio, Control system, Damping- Air friction and Eddy current damping, D'Arsonval Galvanometer- construction, Torque Equation, Dynamic Behavior, Response of Galvanometer, Ballistic Galvanometer, PMMC- Construction, Torque Equation, Voltage/Current Measurement: Ammeter, Voltmeter, Ohmmeter, Multimeter (V.O.M.), Ratio meter, Megger, High frequency Measurement: Q-meter

UNIT - III

AC Bridge: Introduction, Sources and Detectors, General equation for bridge balance, General form of AC Bridge, Maxwell's Bridge, Hay's bridge, Anderson's bridge, De-Sauty's bridge, Schering bridge, Wien's bridge, Electronic Instruments: Introduction, Advantage of Electronic voltmeter, VTVM, Differential voltmeter, Electronic voltmeter using rectifier, True RMS reading voltmeter, Calorimeter power meter.

UNIT - IV

Transducers: Classification of transducer, Primary & Secondary, Passive & Active, Analog & Digital, Potentiometer, loading effect, Strain Gauge, Thermistor, Construction of thermistor, Thermocouple, LVDT, Advantage & Disadvantage of LVDT, RVDT, Capacitive Transducer, Piezo-electric transducer, Hall-effect Transducer, Capacitive Transducer, Pressure Transducer.

UNIT - V

Display devices: Digital display method, Segmental display- 7segment & 14 segment display, dot matrix, LED, LCD, TFT, Plasma display, DLP, Digital voltmeter (DVM): Types of DVM, Ramp type DVM, Integrating type DVM, Potentiometer type (non-integration type), Recorders: Analog Recorder, Null type Recorder, Single point Recorder, Graphical strip chart, X-Y recorders, Magnetic tape recorder, FM recorder, CRO: Introduction, Block diagram, CRT, Functional block diagram of sampling, Storage, Dual trace and dual beam oscilloscope.

SUGGESTED TEXT BOOKS:-

1. Modern Electronic Instrumentation and Measurement Technique, W D Cooper & A D Helfrick, PHI 2000
2. A Course in Electrical and Electronic Measurements and Instrumentation, A K Sawhney Dhanpat Raj & Sons, 2010

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