

CERTIFICATE

This is to certify that Satyendra's thesis, "Modeling of Vacuum Channel Field Effect Transistor" submitted in partial fulfilment of the requirements for the award of a Master of Science Degree in Electronic at Guru Ghasidas Vishwavidyalaya (A Central University), Bilaspur (C.G.), INDIA, is an authentic work carried out by her under my supervision and guidance.

Date

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