

Certificate of Training

DIDDIGI SAIKIRAN

from Guru Ghasidas Vishwavidyalaya ,Bilaspur. has successfully completed a 6-week online training on **VLSI Design**. The training consisted of Introduction to VLSI, HDL Coding Concepts, Combinational Circuits, Sequential Circuit Design, Finite State Machines, System Design using FPGA, and Case Studies & Final Project modules.

We wish DIDDIGI all the best for future endeavours.



Sarvesh Agarwal

FOUNDER & CEO, INTERNSHALA

Date of certification: 2022-08-13

Certificate no. : 95248568-DE16-040B-2518-F5321E57FBB8

For certificate authentication, please visit https://trainings.internshala.com/verify_certificate